EAST Search History

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	"6535811".pn. "5680620". pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/12 17:52
S2	6	"7172712".pn. "20030225866".did. "6256668".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/15 13:49
S3	6	"7127712".pn. "20030225866".did. "6256668".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/15 13:50
S 4	848	717/124-129.ccls. and @ad<"20000726"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 00:36
S 5	70	717/124-129.ccls. and @ad<"20000726" and debug\$ same exception	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 00:50
\$6	27	717/124-129.ccls. and @ad<"20000726" and debug\$ same exception same (processor microprocessor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 00:51
S7	3	717/124-129.ccls. and @ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (illegal unauthorized)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:07

S8	24	@ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (illegal unauthorized)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:08
S9	21	@ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (illegal unauthorized) not S7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:08
S10	2	717/124-129.ccls. and (debug adj logic).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:16
S11	0	"717"/.clas. and (debug adj logic).clm. not S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:17
S12	21	(debug adj logic).clm. not S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:17
S13	70	(717/124-129.ccls. 714/30-39.ccls.) and @ad<"20000726" and debug\$ same exception same (processor microprocessor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:18
S14	10	(717/124-129.ccls. 714/30-39.ccls.) and @ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (illegal unauthorized)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:19

S15	10	(717/124-129.ccls. 714/30-39.ccls.) and @ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (illegal unauthorized)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 13:19
S16	1	(717/124-129.ccls. 714/30-39.ccls.) and @ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (swap\$ thrash\$)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:07
S17	3	@ad<"20000726" and debug\$ same (interrupt exception) same (processor microprocessor) same (swap\$ thrash\$)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:08
S18	22	@ad<"20000726" and debug\$ same (swap\$ thrash\$) with (memory storage ram)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:09
S19	317	@ad<"20000726" and detect\$ same (swap\$ thrash\$) with (memory storage ram)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:09
S20	85	("717".clas. "714".clas.) and @ad< "20000726" and detect\$ same (swap\$ thrash\$) with (memory storage ram)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:10
S21	85	("717".clas. "714".clas.) and @ad<"20000726" and detect\$ same (swap\$ thrash\$) with (memory storage ram) not (S18 S8)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:13
S22	13	("717".clas. "714".clas.) and @ad< "20000726" and debug\$ and detect\$ same (swap\$ thrash\$) with (memory storage ram) not (S18 S8)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/17 14:13

6/22/2009 10:39:20 AM

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